

APPLICATION DATA SHEET**Inventor Information**

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Application Information

Title Line One:: SEMICONDUCTOR MEMORY DEVICE HAVING
DUTY CYCLE CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT
INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR MEMORY DEVICE
Title Line Two::
Total Drawing Sheets:: 6
Formal Drawings?: YES
Suggested Drawing Figure:: FIG. 5
Application Type:: UTILITY
Suggested Classification::
Suggested Group Art Unit::
CD-ROM or CD-R?: No
Request for Early Publication?: No

Initial Application Data Sheet Page 1
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Request for Non-Publication?: NO
Small Entity?: NO
Petition Included?: No
Docket Number: 9898-293

Representative Information

Representative Customer Number: 20575

Continuity Information

This application is a:
>Application One:
Filing Date:

Prior Foreign Application

Foreign Application One: 2002-53327
Filing Date: 09/04/02
Country: Republic of Korea
Priority Claimed: YES

Assignee Information

Assignee Name: Samsung Electronics Co., Ltd.